

PATENT
W&B Ref. No. : INF 2129-US
Atty. Dkt. No. INFN/WB0054

IN THE SPECIFICATION:

Please replace paragraph [0021] with the following amended paragraph:

[0021] The useful circuit 7 has a signal path 8, on which a circuit-internal signal is transferred. The circuit-internal signal serves for driving a timing-sensitive circuit, e.g. a memory circuit in which setup and hold times have to be complied with. Delay elements 9a, 9b, 9c are introduced in the signal path, which delay elements, under the control of a delay control unit 10, are either switched into the signal path, so that the circuit-internal signal is delayed, or are bridged in such a way that the circuit-internal signal is not delayed by the respective delay element 9a, 9b, 9c.